

Year 2020 Accepted/Published	
1	Barhate, V. N., Agrawal, K. S., Patil, V. S., Patil, S. R., & Mahajan, A. M. (2020). Performance enhancement of Al/La ₂ O ₃ /ZrO ₂ /4H-SiC MOS device with LaON as interfacial passivation layer. <i>Materials Science in Semiconductor Processing</i> , 117, 105161.
2	Agrawal, K. S., Barhate, V. N., Patil, V. S., Patil, L. S., & Mahajan, A. M. (2020). Plasma-enhanced atomic layer-deposited La ₂ O ₃ ultra-thin films on Si and 6H-SiC: a comparative study. <i>Applied Physics A</i> , 126(8), 1-10.
3	Agrawal, K., Patil, V., Barhate, V., Yoon, G., Lee, Y. J., Mahajan, A., & Yi, J. (2020). Temperature-dependent study of slow traps generation mechanism in HfO ₂ /GeON/Ge (1 1 0) metal oxide semiconductor devices. <i>Solid-State Electronics</i> , 167, 107797.
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8	Rondiya, S. R., Jadhav, C. D., Chavan, P. G., & Dzade, N. Y. (2020). enhanced field emission properties of Au/SnSe nano-heterostructure: A combined experimental and theoretical investigation. <i>Scientific reports</i> , 10(1), 1-10.
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10	Pandit, B., Jadhav, C. D., Chavan, P. G., Tarkas, H. S., Sali, J. V., Gupta, R. B., & Sankapal, B. R. (2020). Two-dimensional hexagonal SnSe nanosheets as binder-free electrode material for high-performance supercapacitors. <i>IEEE Transactions on Power Electronics</i> , 35(11), 11344-11351.
11	Tarkas, H. S., Tak, S. R., Deo, V. V., More, S. A., Upasani, D. P., Ghosh, S. S., & Sali, J. V. (2020). A New Approach for One-step Synthesis of Perovskite: fullerene Bulk Heterojunction Using Surfactant Free Microemulsion in Slot Die Method.
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